

FIG. 1a - Prior Art

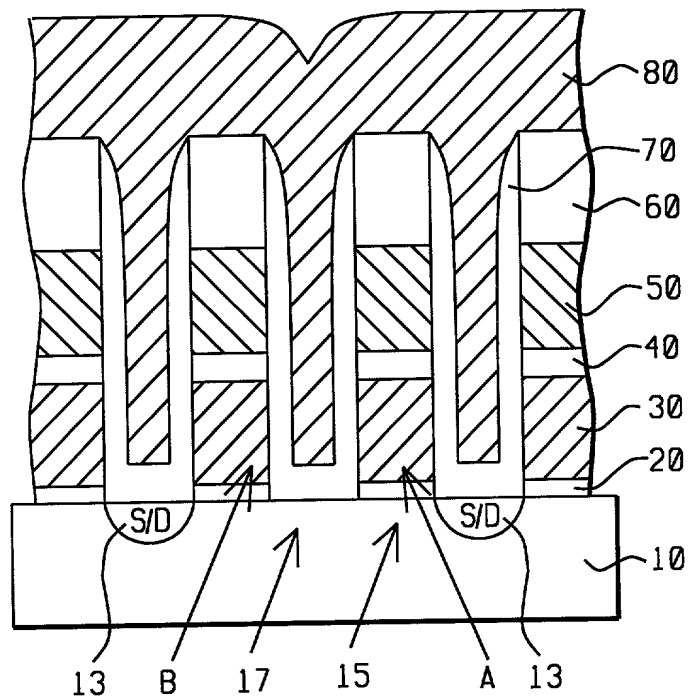


FIG. 1b - Prior Art

Table of Keys to Figures

D : drain as BIT LINE
 S : source
 CG: control gate
 SG: selected gate as WORD LINE
 TG: transfer gate



: selected cell



: un-selected cell

FIG. 1c - Prior Art

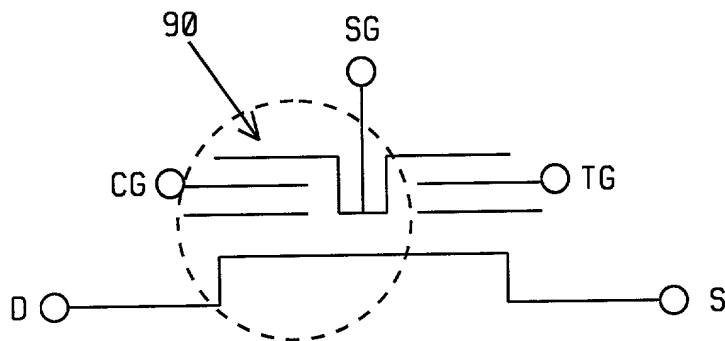


FIG. 1d - Prior Art

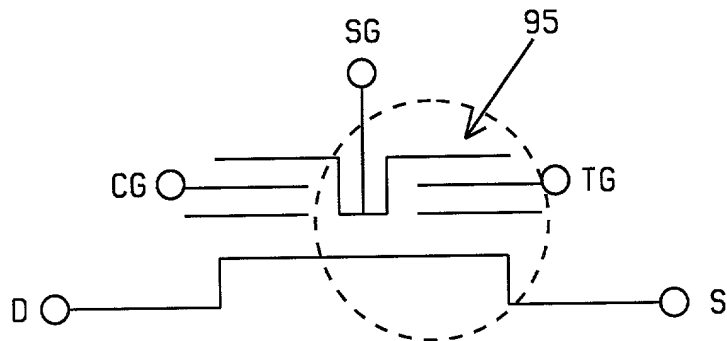


FIG. 1e - Prior Art

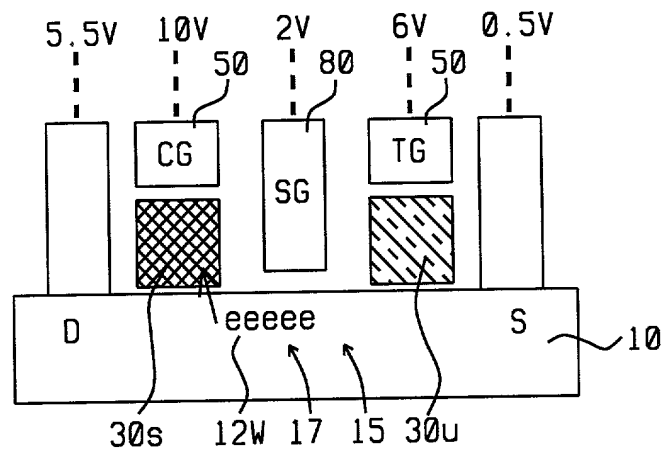


FIG. 2a - Prior Art

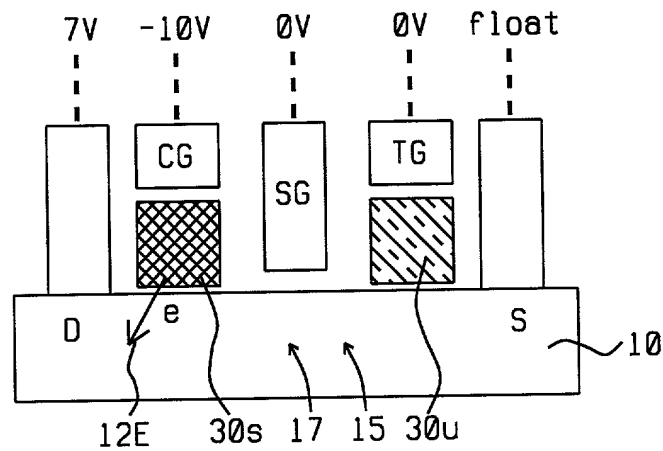


FIG. 2b - Prior Art

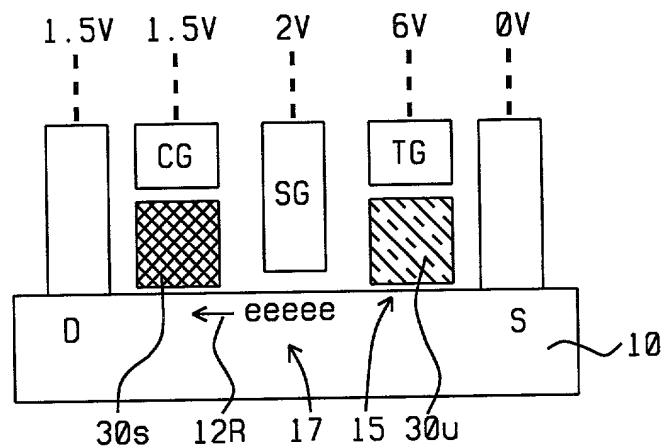


FIG. 2c - Prior Art

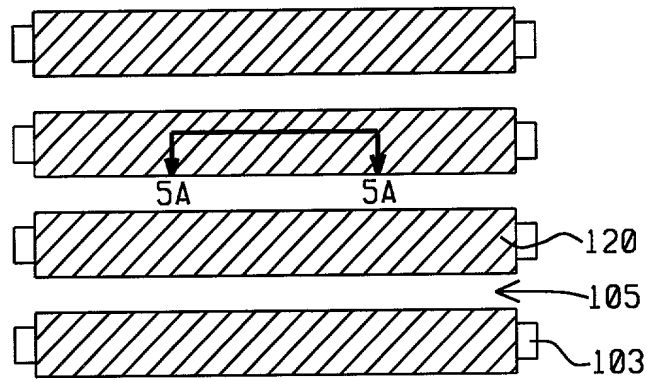


FIG. 4a

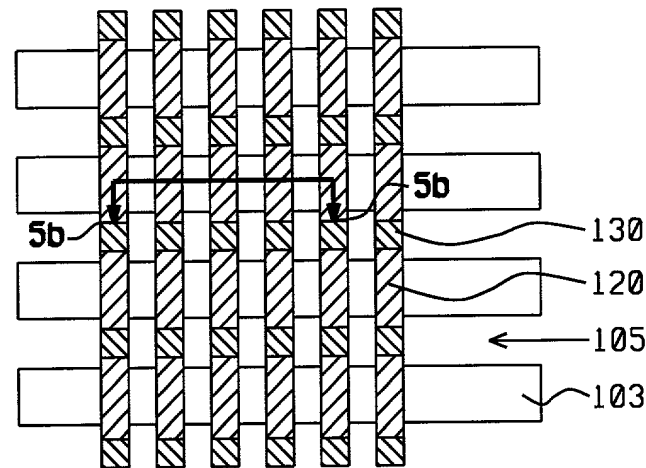


FIG. 4b

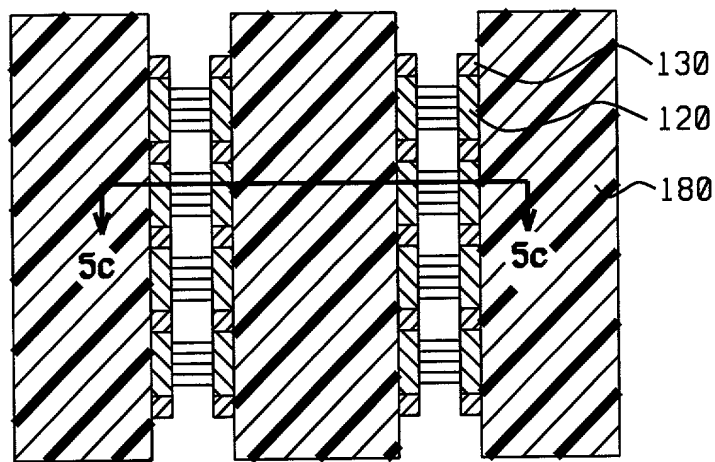


FIG. 4c

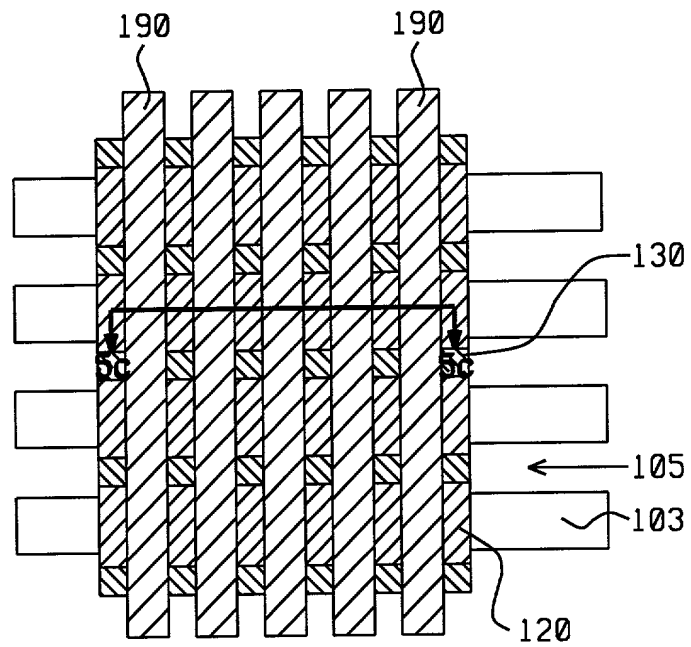


FIG. 4d

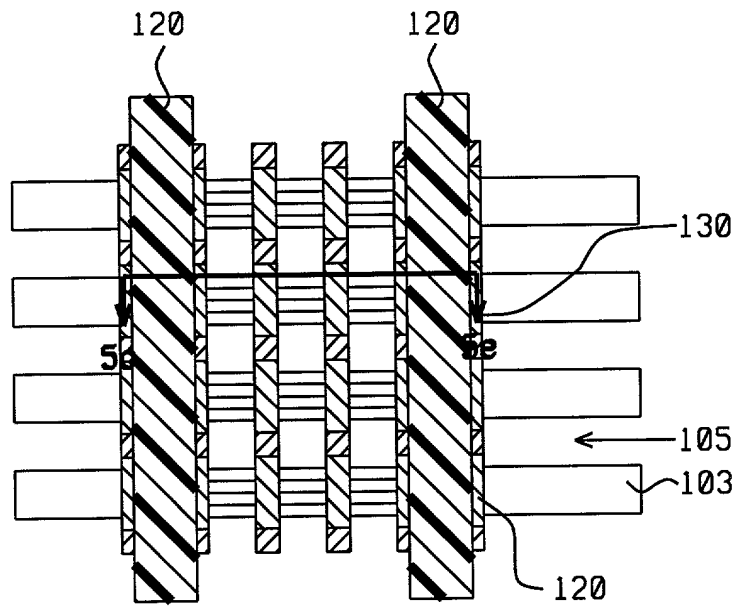


FIG. 4e

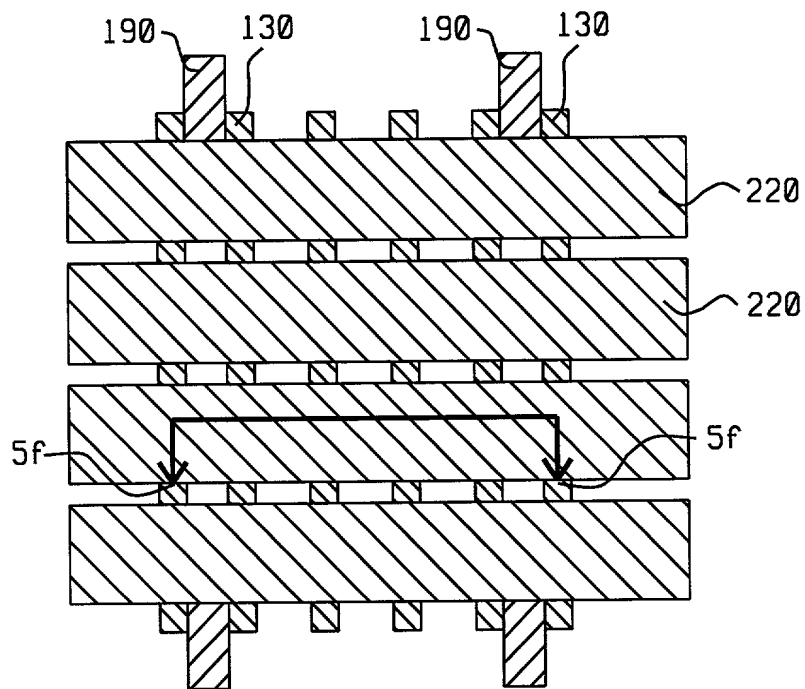


FIG. 4f

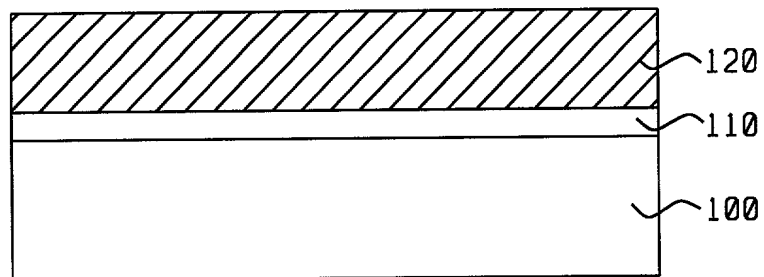


FIG. 5a

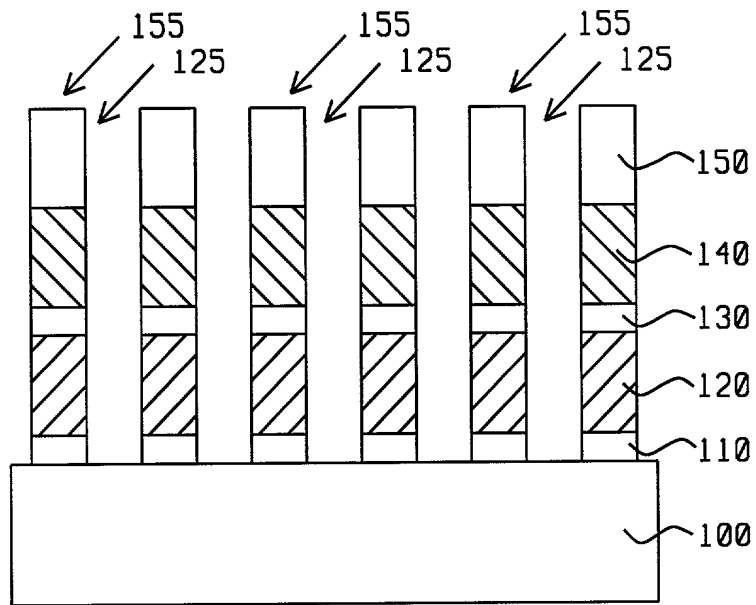


FIG. 5b

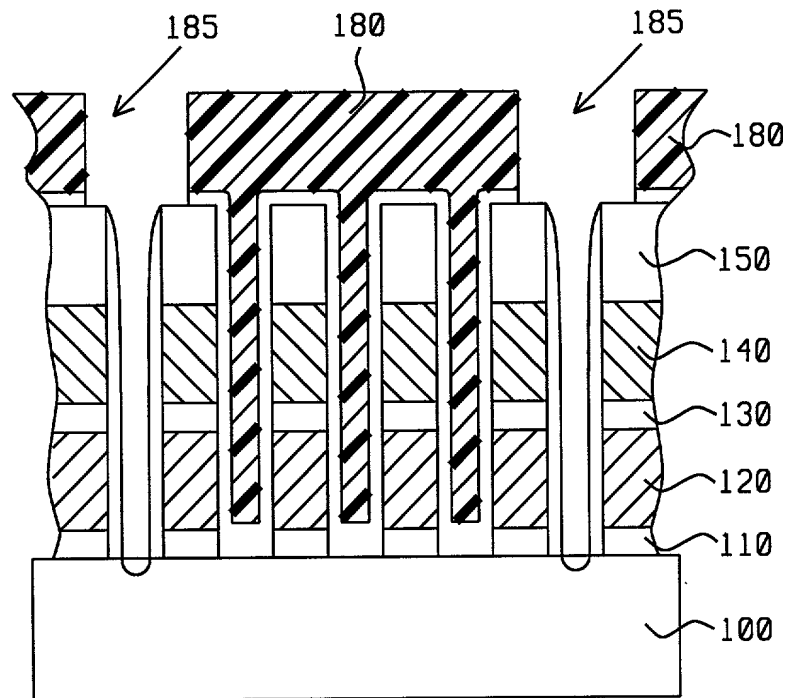


FIG. 5c

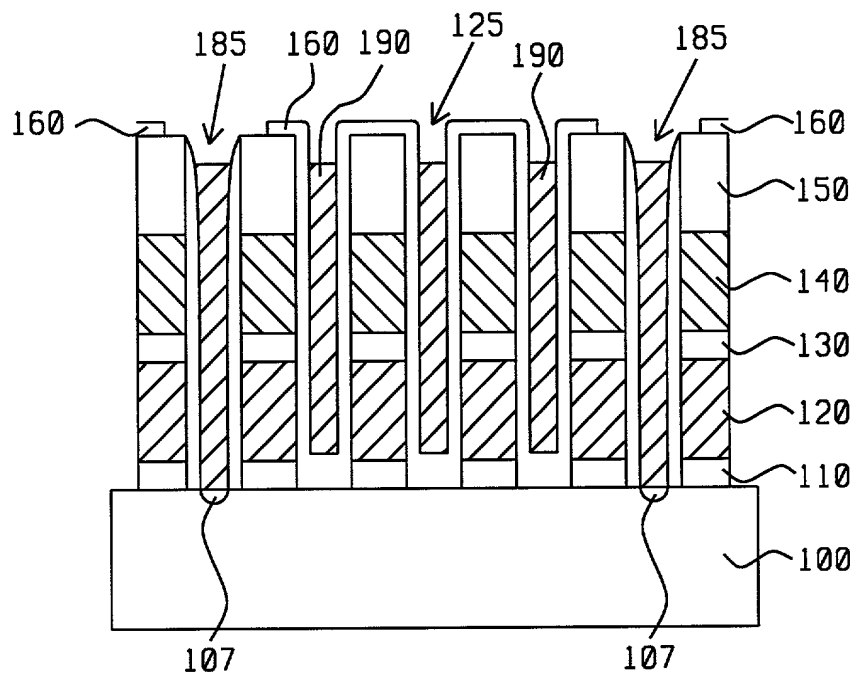


FIG. 5d

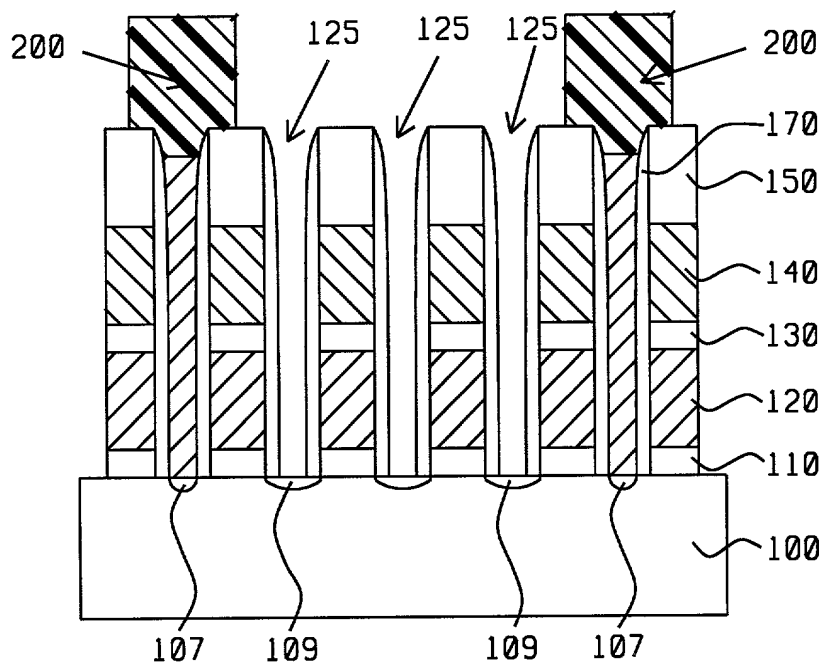


FIG. 5e

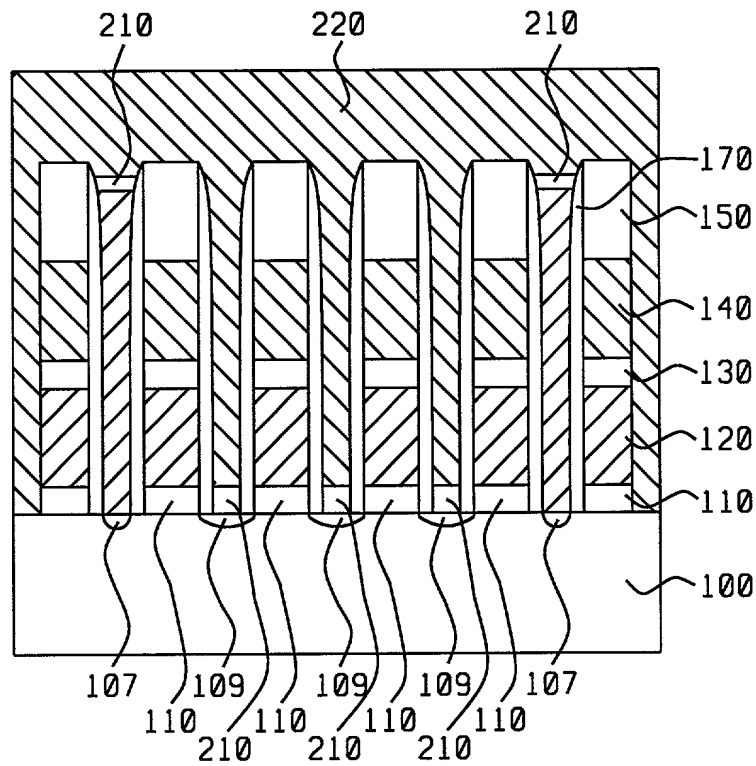


FIG. 5f

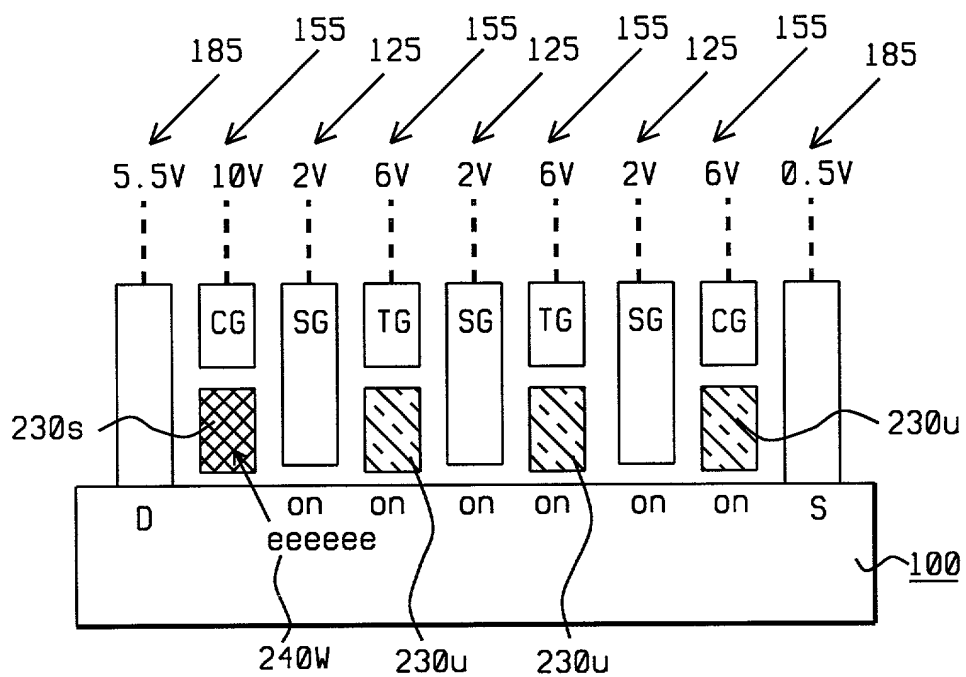


FIG. 6a

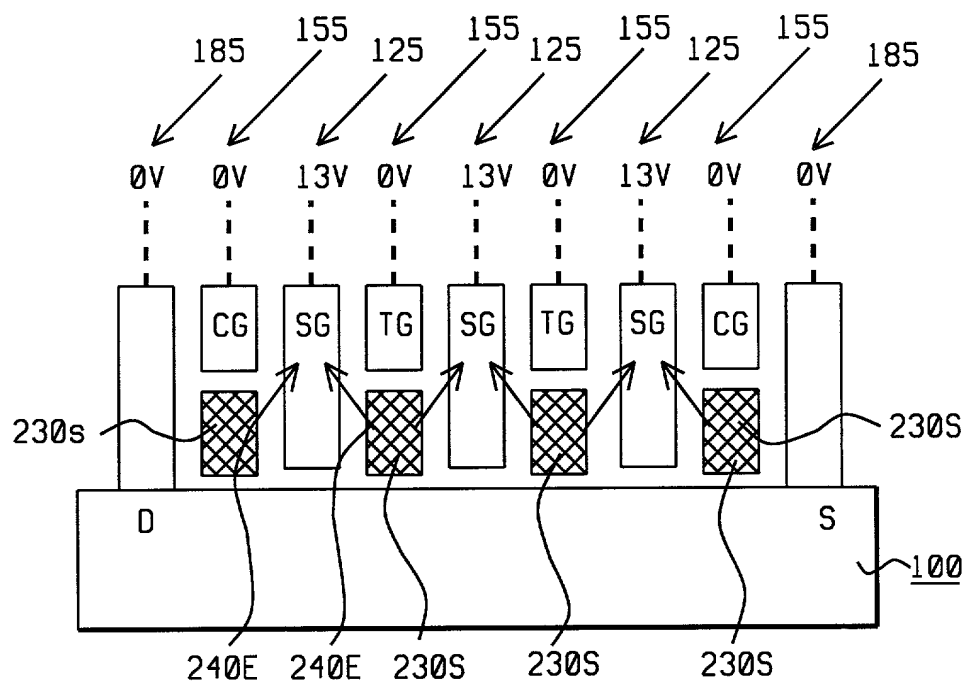


FIG. 6b

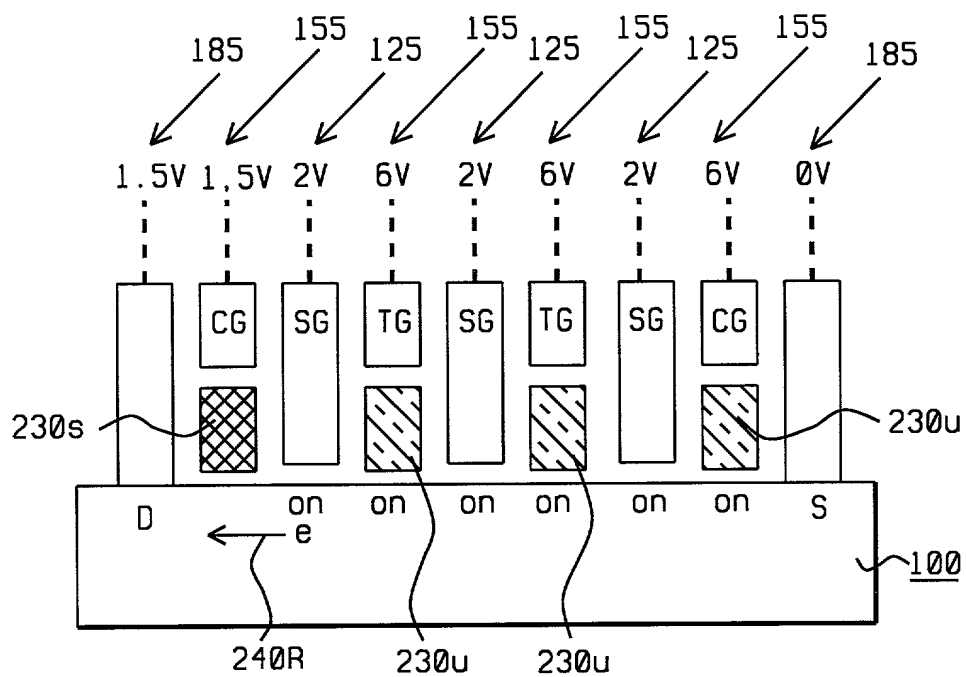


FIG. 6c

